

| LMxBMP ALT6 | LMxBMP ALT5 | LMxBMP ALT4 | LMxBMP ALT3 | LMxBMP ALT2 | LMxBMP ALT1 | LMxBMP ALT0 | DTS Enableable ALT | Default Functions | LMxBMP Name(*) | Name | CM4 CARRIER 40 pin GPIO mapping | Name | LMxBMP Name(*) | Default Functions | DTS Enableable ALT | LMxBMP ALT0 | LMxBMP ALT1 | LMxBMP ALT2 | LMxBMP ALT3 | LMxBMP ALT4 | LMxBMP ALT5 | LMxBMP ALT6 |
|-------------|-------------|-----------------|-------------|-------------|---------------|---------------|--------------------|-------------------|----------------|-------|---------------------------------|--------|----------------|-------------------|--------------------|--------------|--------------|---------------|---------------|-------------|-------------|-----------------|
| | GPIO3_I025 | | I2C5_SDA | PWM1_OUT | SAI1_TX_BCLK | SAIS_MCLK | ALTS | I2C5_SDA | GPIO3.I025 | GPIO2 | 1 | 3V | | | | | | | | | | |
| | GPIO3_I021 | PDM_BIT_STREAM0 | I2C5_SCL | PWM2_OUT | SAI1_TX_DATA2 | SAIS_RX_DATA0 | ALTS | I2C5_SCL | GPIO3.I021 | GPIO3 | 2 | SV | | | | | | | | | | |
| | GPIO2_I008 | UART3_TX | I2C2_SCL | | | USDHCl_DATA0 | ALTS | GPIO | GPIO2.I008 | GPIO4 | 3 | GND | | | | | | | | | | |
| | | | | | | | | | | | 8 | GPIO18 | GPIO4.I021 | UART1_TX | ALTS | SAI2_RX_SYNC | SAIS_TX_SYNC | | | | | |
| | | | | | | | | | | | 9 | GPIO15 | GPIO4.I022 | UART1_RX | ALTS | SAI2_RX_BCLK | SAIS_TX_BCLK | SAIS_TX_DATA1 | SAI2_RX_DATA1 | UART1_TX | GPIO4_I021 | PDM_BIT_STREAM2 |
| | | | | | | | | | | | 10 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | FLEXCAN2_TX | UART1_RX | GPIO4_I022 | PDM_BIT_STREAM1 |
| | | | | | | | | | | | 11 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 12 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 13 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 14 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 15 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 16 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 17 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 18 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 19 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 20 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 21 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 22 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 23 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 24 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 25 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 26 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 27 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 28 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 29 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 30 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 31 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 32 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 33 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 34 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 35 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 36 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 37 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 38 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 39 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |
| | | | | | | | | | | | 40 | GPIO18 | GPIO5.I013 | GPIO | ALTS | ECSPI2_S0 | UART4_RTS_B | I2C4_SDA | COM_CLK02 | GPIO5_I013 | | |

| Name | LMxBMP Name(*) | Default Functions | DTS Enableable ALT | LMxBMP ALT0 | LMxBMP ALT1 | LMxBMP ALT2 | LMxBMP ALT3 | LMxBMP ALT4 | LMxBMP ALT5 | LMxBMP ALT6 |
|--------|----------------|-------------------|--------------------|-------------|--------------------------|-------------|-------------|-----------------------------|-------------|-------------|
| GPIO44 | GPIO5.I017 | I2C2_SDA | ALTS | I2C2_SDA | ENET_QOS_1588_EVENT1_OUT | USDHCl_WP | ECSPI1_S0 | | GPIO5_I017 | |
| GPIO45 | GPIO5.I016 | I2C2_SCL | ALTS | I2C2_SCL | ENET_QOS_1588_EVENT1_IN | USDHCl_CD_B | ECSPI1_MISO | ENET_QOS_1588_EVENT1_AUX_IN | GPIO5_I016 | |

NOTES:
 *Column LMxBMP Name indicate GPIO names referring to LMxBMPplus Reference Manual. The enumeration starts from 1.
 Linux gpiochip are enumerated differently starting from zero, so e.g. GPIO2.I001 corresponds to gpiochip1 line 0.

